

Amity School of Engineering & Technology
Department of Electronics & Communication Engineering
Board of Studies
Minutes of Meeting

Dated: 7th Nov 2020

The Board of Studies meeting was held online on MS Teams at 2 p.m. on 7th Nov 2020. The meeting was held for proposal of revision in the syllabus for programme B.Tech.-ECE & B.Tech.-EEE (Semester 5-8) and designing of new syllabus for B.Tech. (Electronics & Computer Engineering) (for semester 1-4). The meeting was attended by the following:

Internal Members:

1. Dr. S. N. Sridhara, Director , ASET, AUH
2. Dr. Anil Kumar, Associate Professor & Head, ECED, Dy. Director-IQAC, AUH.
3. Dr. Brijesh Kumar, Associate Professor & Head, AINT.
4. Dr. Sanjeev Sharma, Associate Professor & MED, ASET.
5. Dr. Mahesh Kumar, Associate Professor, ECED, ASET.
6. Mr. Manoj K Pandey, Assistant Professor-III, ECED, ASET.
7. Dr. Neeraj Gupta, Assistant Professor-II, ECED, ASET.
8. Dr. Ved Prakash, Assistant Professor-I, ECED, ASET.
9. Ms. Nisha Charaya, Assistant Professor-I, ECED, ASET.
10. Ms. Manjeet Kaur, Assistant Professor-I, ECED, ASET.
11. Dr. Arun K Singh, Assistant Professor-I, ECED, ASET.

External Experts:

1. Mr. Rahul Kundu, IAM Consultant, Cyber Security Practice, TCS, London, UK.
2. Ms. Rajni Gaud, Sr. Integration Development Lead in Onsite Role, Cognizant Technology Solutions, Dubai, UAE.

The following are the suggestions proposed by the members:

B.Tech. (Electronics & Communication Engineering):

1. 3rd Semester

- Virtual Instrumentation Lab: Few experiments based on the interfacing deck can be included in the syllabus.
- Digital Electronics and its Lab: Syllabus should be revised and applications in the industry can be included.

2. 4th Semester

- Electronic Workshop & PCB Lab: Few experiments based on the OrCAD should be included in the syllabus. Syllabus of PCB Fabrication should be inline with the Lab.

3. 5th Semester

- Digital Circuits & Systems: In place of VHDL, syllabus should be based on Verilog Programming.
 - Control Systems: State Space Variables need to be included. In Control Systems Lab few experiments based on Simulink and applications of PID controllers need to be incorporated.
 - Microprocessor System: 8085 processors based topics can be reduced and latest processors should be included. Microprocessors System Lab should be inline with the theory.
4. **6th Semester**
- VLSI Design: VLSI Fabrication should be the part of syllabus. In VLSI Design Lab, few more experiments need to be included.
 - Embedded System Lab: Few more experiments related to ARM processors and applications in the real world should be the part of syllabus.
5. **7th Semester**
- Radar and Satellite Communications Lab: Few experiments need to be revised.
6. **7th Semester**
- Wireless Communications: There is overlap of the some of the topics in Mobile Communications and Wireless Communications that can be removed.

B.Tech. (Electrical & Electronics Engineering):

1. **4th Semester**
- Electromagnetic Field Theory: The Topic Corona can be replaced with Smith chart.
2. **5th Semester**
- Power System-I: small revision in module-2 is required.
3. **6th Semester**
- Modern and Digital Control Engineering: Topic on State Space Variables need to be removed.
4. **7th Semester**
- Measurements and Measuring Instruments: This subject can be suitably sifted in the earlier semesters.
 - C Bases Embedded System Design and its Lab: Syllabus needs to be revised.

B.Tech. (Electronics & Computer Engineering):

1. **4th Semester**
- Analog & Digital Communications: Some revision in syllabus is required.
 - Microprocessor & Microcontrollers: Small change in syllabus need to be update.

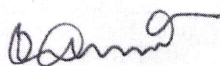
General Suggestion: Syllabus of subjects which are common in above programmes should be kept same.

Embedded System (Flexi Track):

- Introduction to Microprocessor Systems: Some topics related to Digital Electronics can be included in the syllabus.
- For Subjects viz. PCB Fabrication, Robotics & Automation, Simulation & Modelling Lab, it is suggested that either these subjects can be replaced with some other suitable subjects or syllabus should be revised.



Dr. Anil Kumar
Associate Professor & Head, ECED
Dy. Director-IQAC.
AUH



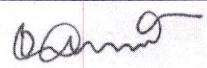
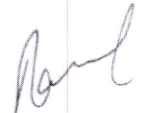

Dr. S.N. Sridhara
Director, ASET
AUH

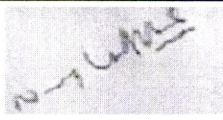

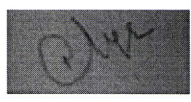

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Attendees Signature Sheet

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Sr. No.	Name	Designation	Organization	Signature
1	Dr. S N Sridhara	Director, ASET	AUH	
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