Role : Software Developer

Qualification : B.Tech (Electrical/ Electronics)

Academic Criteria : 75% or above in 10th, 12th, B.Tech.

Job Location : NOIDA

Package : 4-5 lacs p.a depending on their performance in test, technical/HR evaluation

Recruitment Process : Written test, 2 technical rounds, 1 HR round.

Venue for the process : Will be confirmed to shortlisted students.

About Interra Systems:

Interra Systems is a diversified software products and services provider for Digital Media and Semiconductor industries for over fifteen years. Interra solutions are differentiated by proven engineering expertise, solid track record in collaborating with customer partners on advanced technologies, and superior responsiveness. Interra’s Digital Media solutions are led by Baton, an automated file-based QC solution, by Vega, a family of media analyzers, and by Orion, real-time content monitoring solution. Interra’s services for Semiconductor industries are focused on Design Implementation, EDA & CAD Flow, Library Development, Memory Design, and Verification. These services are further leveraged by Interra’s technologies in Characterization, Memory Development, HDL Analysis, and Synthesis. Interra is headquartered in Cupertino, CA with engineering centers in India.

EDA Technologies:

Interra Design Automation technologies are led by standards-compliant EDA building blocks (EDA Objects), HDL coverage test suites and Memory compiler development system (MC2) and Memory Design Framework (Touchstone).
Our technologies are widely used by major SoC Design Houses and EDA Vendors. Interra’s technologies accelerate service deployment, reduce development time and improve productivity for VLSI designers, Design Flow Automation teams and EDA tool developers. These technologies also lay the foundation for a talented design pool of engineers who bring significant value to design services.

Interested Students need to apply by clicking on below link.

https://docs.google.com/forms/d/1myJXO0yhiFQu643Vrm3_e6pQIjln5lYH37VQRNpURUM/viewform?c=0&w=1

Last date to apply is 20th July 2015, 10.00 am

All the Best

Dr. Ajay Rana

Director- ATPC