

## VLSI & DIGITAL SYSTEM DESIGN CLUB

1. Name of the Club : VLSI & Digital System Design Club
2. Level of the Club : University/Domain
3. Description of the Club : VLSI and Digital System Design Club members and participants have the opportunity to engage in discussions and explore the latest developments in Very Large-Scale Integration (VLSI) technology. VLSI is most promising domain in current engineering developments. Through the club activities, participants will gain valuable insights into the advancements, trends, and practical applications in the field of VLSI. The club will foster a collaborative environment, allowing attendees to exchange ideas and perspectives on VLSI design and implementation, contributing to their knowledge and understanding of this critical domain. The club activities will let student to practice both front end and back end VLSI design tools and technology along with sessions from industry experts which will guide them for thier placements in emerging semiconductor industry. The club will unveil opportunities for students to participate in Hackathons and design contests as teams.
4. Objective of Club : To equip club members with knowledge of emerging trends in VLSI Design and to apply the gained knowledge for design and implementation of digital system design, hosting of VLSI design-based competitions and further motivate students for participation in inter-institute competitions.

Designation in the Club	Name	Faculty Id	E-Mail Id	Mobile Number
Chair	Dr. Nidhi Gaur	3948	ngaur@amity.edu	9312482592
Co -Chair	Ms. Shikha Bathla	6752	sbathla@amity.edu	9999457542

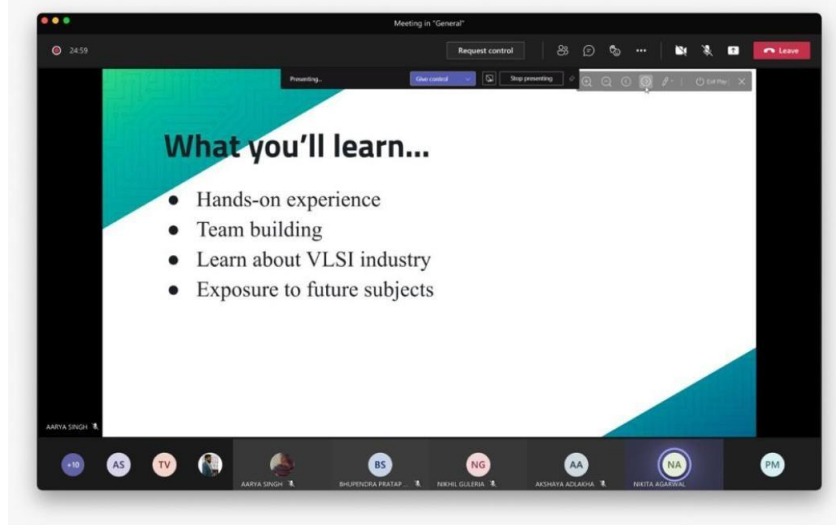
### 5. Details of the Coordinating Students:

Designation in the Club	Name of the Student	En No	Program me and Batch	Name of the Institute	E-mail Id	Mobile Number
President	Vanshika Sharma	A2305121004	Btech ECE and 2021-25	ASET	vanshika2003sharma@gmail.com	9310343567
Vice President	Diptangshu Parasor	A2305122007	Btech ECE and 2022-26	ASET	<a href="mailto:parasordip@gmail.com">parasordip@gmail.com</a>	8638365557
Treasurer	Haifa Salim	A2305122010	Btech ECE and 2022-26	ASET	<a href="mailto:haifasalimrawther@gmail.com">haifasalimrawther@gmail.com</a>	9899229293
Member Secretary	Dhruv Arora	A2305122004	Btech ECE and 2022-26	ASET	<a href="mailto:dhruvarora310@gmail.com">dhruvarora310@gmail.com</a>	9582473029
Vice President (Female)	Sneha Saxena	A2305122017	Btech ECE and 2022-26	ASET	<a href="mailto:sneha.saxena1213@gmail.com">sneha.saxena1213@gmail.com</a>	9099512352

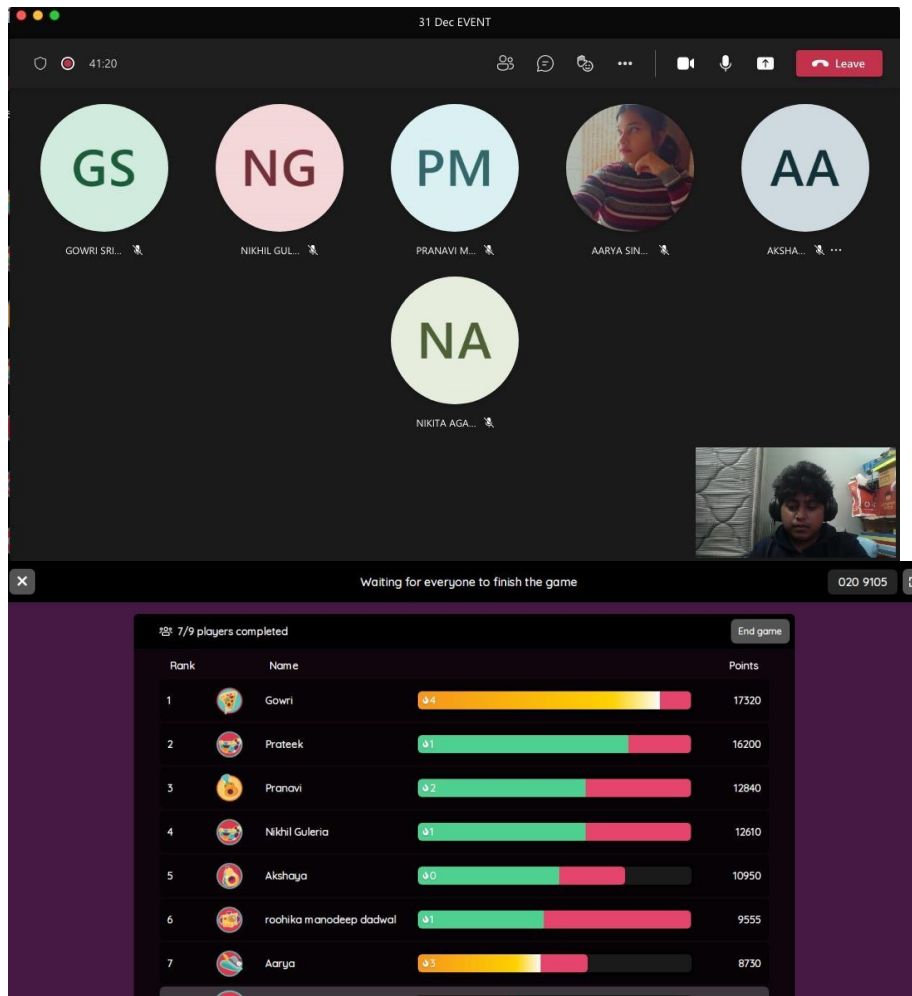
### Events organized for the academic year 2021-2022:

- UNMASKING VLSI: (2<sup>nd</sup> September 2021, 2:30 PM onwards on MS Teams)  
An introductory event was held where seniors briefed about the VLSI club.

Interesting quizzes were held. It was an interesting session overall. The main objective was to give a brief description about the scope in VLSI. It shows the gateway of VLSI industry to know about digital electronics and circuit simulation.



- DIGIPEDIA: (31<sup>st</sup> December 2021, 4PM onwards on MS Teams)**  
 In the event a quiz was conducted on the basics of electronics. Questions related to basics of electronic circuits were asked in an MCQ format on an online quizzing platform. The quiz provided an exuberating experience to the participants and inculcated friendly competition among them.



- MASTER MIND (14<sup>th</sup> January 2022, 4:30PM onwards on MS Teams)  
In the event the seniors briefed about the tips and tricks for campus placements. The 4<sup>th</sup> year students shared their experiences regarding how to crack good companies and prepare for campus interview. It was an interesting session overall. The main objective was to give a brief description about the placement and future opportunities.

**AMITY SCHOOL OF ENGINEERING AND TECHNOLOGY**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATIONS**  
**VLSI AND DIGITAL DESIGN CLUB**

# MASTER MIND

A Session on tips and tricks for Campus Placements

**REGISTER NOW!**

DATE: 14/1/2022  
TIME: 4:30 PM Onwards  
TEAM CODE: bwv7mqn

FOR ANY QUERIES CONTACT:  
**ROHAN SRINIVASAN** (+ 91 9818540000)  
**KHUSHI GUPTA** (+ 91 9205664081)  
**GOWRI SRINIVASAN** (+91 8860744687)  
at vlsiclubofficial@gmail.com

GS VT NG TV RD NA PM

## The Interviews

You've cleared the tests? Great, now comes the make or break part.

- Be properly dressed in formals, remember presentation is key! Hair cut, Beard trimmed or shaved.
- Remember your resume thoroughly, only keep info and achievements which you can back up and justify!
- Have an intro ready, how you explain about yourself and speak matters a lot! (stuttering is okay but not all the time 😊)
- Read about the company (their profits, rivals, what they do etc.) and the profile you're applying for beforehand.

Prepare on questions related to the profile, read online to get an idea!  
I know it sounds tedious to go through such lengths but look at it from the company's perspectives. Hiring is hard, So be prepared and shape your goals in a way so it sounds in-line with the company's ideology!

NG VT GS TV RD NA PM

- **TECHNO-LENS (26<sup>th</sup> January 2022 on Instagram)**  
An open photography competition held for school and college students from all over the country. Participation and assessment all happened on the Official VLSI club Instagram page @vlsiclub.

**AMITY UNIVERSITY**  
**AMITY SCHOOL OF ENGINEERING AND TECHNOLOGY**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATIONS**  
**VLSI AND DIGITAL SYSTEM DESIGN CLUB**  
PRESENTS

# TECHNO-LENS

**LAST DATE TO REGISTER AND SEND ENTRIES: 30<sup>TH</sup> JANUARY 2022 (MIDNIGHT)**

**ENTRIES WILL BE POSTED ON INSTAGRAM PAGE**

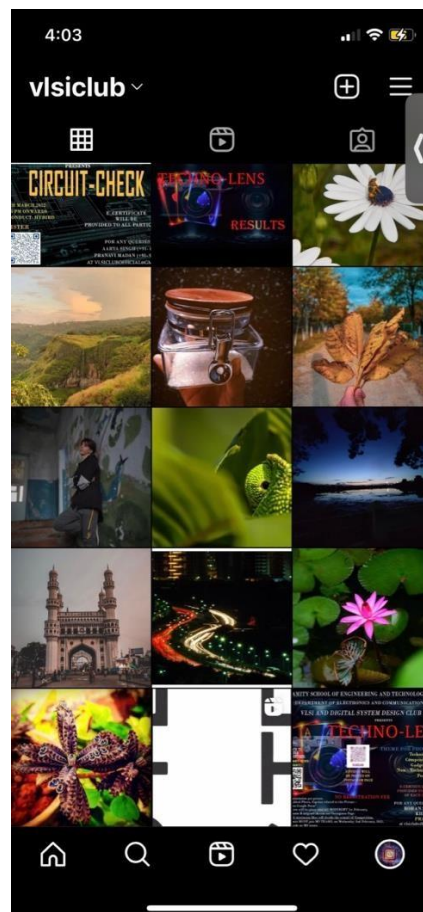
**THEME FOR PHOTOGRAPHY:**  
Technical: Electronic Components, Machines, Gadgets, Phenomena  
Non - Technical: Abstract, Portraits, Nature

**E-CERTIFICATE WILL BE PROVIDED TO ONE WINNER OF EACH CATEGORY**

**FOR ANY QUERIES CONTACT:**  
**ROHAN SRINIVASAN**  
**KHUSHI GUPTA**  
**PRATEEK GOEL**  
at vlsiclubofficial@gmail.com

**RULES:-**  
~ Only ONE submission per person.  
~ Original + Edited Photo, Caption related to the Picture - @ "Upload on Google Form".  
~ The participants will be given time till MIDNIGHT 1st February, to get maximum & original likes on our Instagram Page.  
~ The post with maximum likes will decide the winner of Competition.  
~ All participants MUST join MS TEAMS, on Wednesday 2nd February, 2022, at 5pm onwards on MS teams

**NO REGISTRATION FEE**



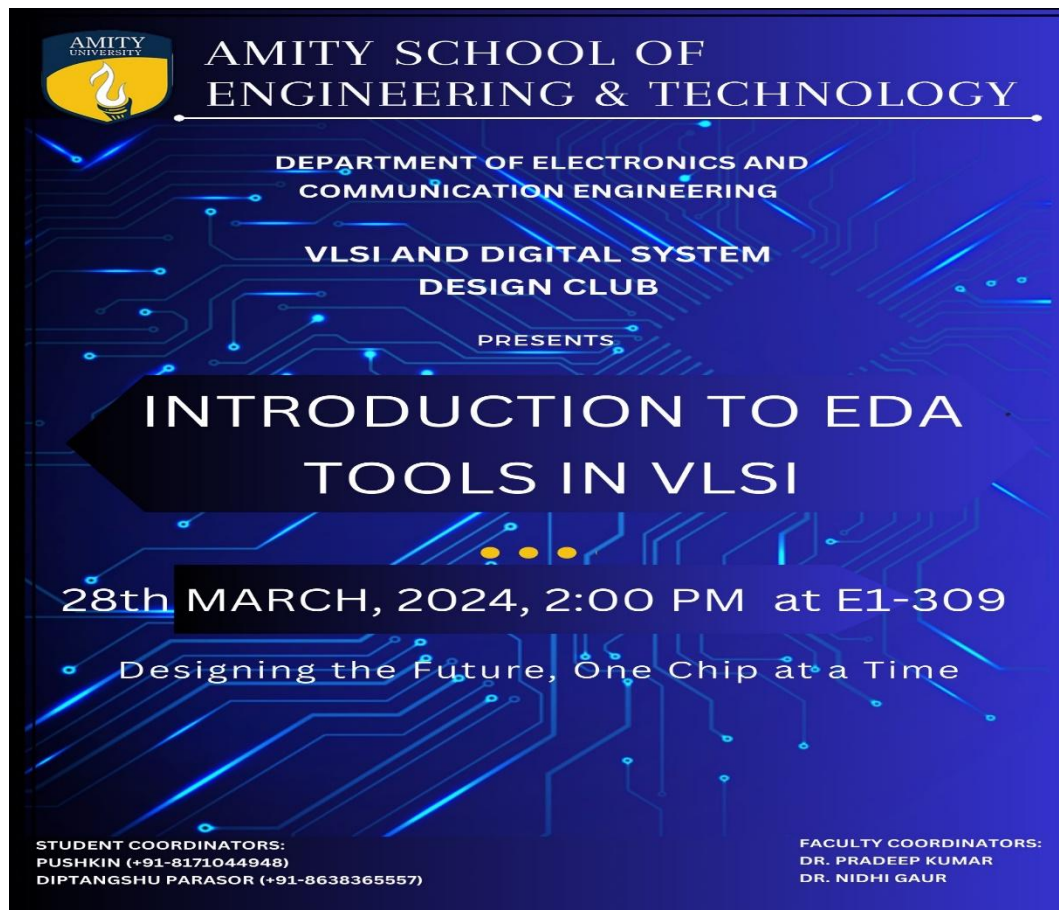
- CIRCUIT-CHECK (17<sup>th</sup> March 2022 on MS Teams)**  
 An interesting competition based on debugging circuit diagrams. Students were provided with circuit diagram, each having components missing or mis-placed, and it was up-to them to figure out what was wrong!

The screenshot shows an MS Teams meeting interface. The main window displays two circuit diagrams for debugging. On the left, titled "Voltage Regulator Circuit", is a bridge rectifier circuit. It has an AC input, a bridge of four diodes, a 470 $\mu$ F capacitor in parallel with the positive output line, a 1k $\Omega$  resistor in series with the positive output line, and a 140 $\mu$ F capacitor in parallel with the output. On the right, titled "Half Wave Precision Rectifier Circuit", is a circuit with a 40Hz AC source, a diode in series with the positive input of an op-amp, a 1k $\Omega$  resistor from the op-amp's non-inverting input to ground, a 1k $\Omega$  resistor from the op-amp's output to its inverting input, and another 1k $\Omega$  resistor from the op-amp's output to the positive output line. A small inset window shows a "Circuit Check@ 17 mar 2022" notification with a green circle and the initials "GS". The right sidebar shows a list of participants: VEDANTA B., AKSHAYA A., TK, TINA KALITA, TANU SINGH, NA, GS, NIKITA AGA, GOWRI SRI, PN, YR, PRATIBHA, YASHASVI, and AARIYA SIN. The bottom left corner shows the name "AARIYA SINGH".

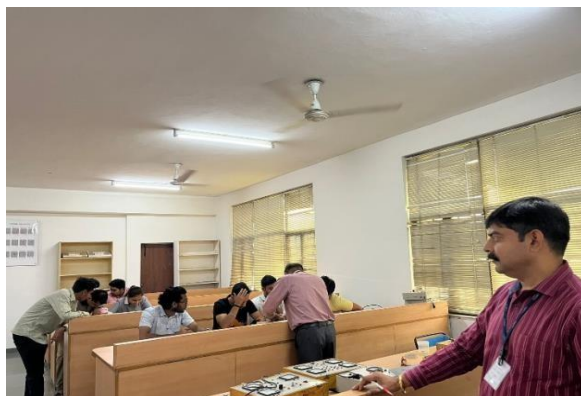
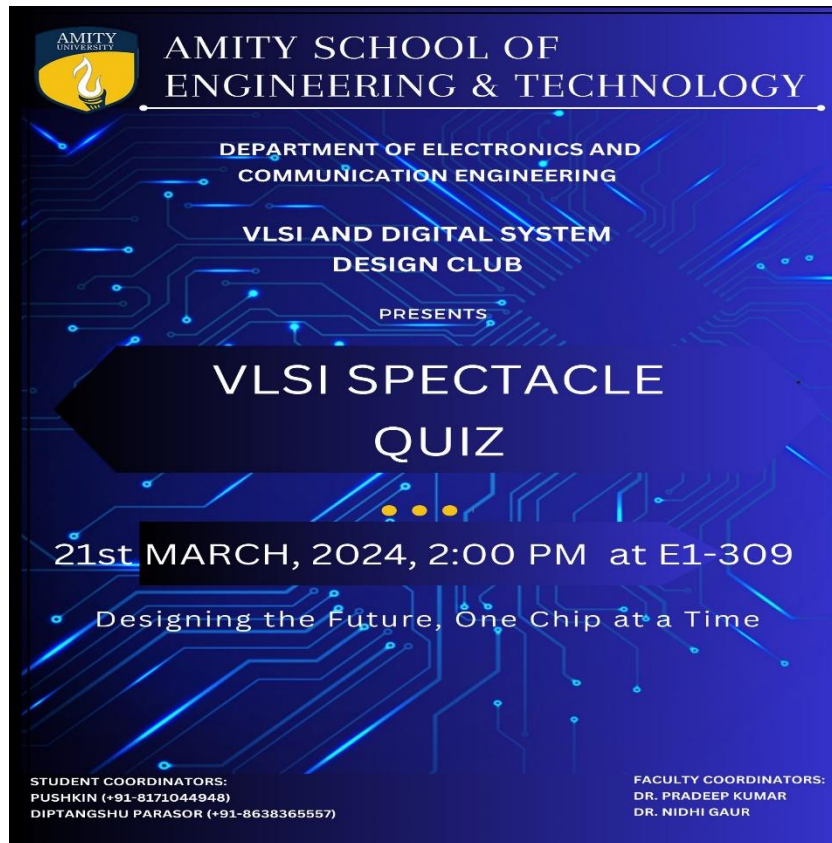


### Events organized for the academic year 2023-2024:

1. **INTRODUCTION TO EDA TOOLS IN VLSI:** On March 28, 2024, the Electronics and Communication Engineering (ECE) Department at ASET, AUUP, hosted a valuable event focused on introducing Electronic Design Automation (EDA) tools in VLSI. The event aimed to provide students and participants with practical insights and knowledge about various EDA tools that are critical in the design and development of VLSI systems, preparing them for advanced applications in the electronics field.



2. **VLSI SPECTACLE QUIZ:** The VLSI Spectacle Quiz, a prominent event focusing on Very Large Scale Integration (VLSI) technology, took place on March 21, 2024, at Amity University Uttar Pradesh (AUUP). This educational and competitive event was meticulously organized by the Electronics and Communication Engineering (ECE) Department of AUUP's Amity School of Engineering and Technology (ASET). The quiz aimed to enhance the knowledge and practical skills of participants in the field of VLSI, fostering a deeper understanding and appreciation of advanced electronics and semiconductor design.



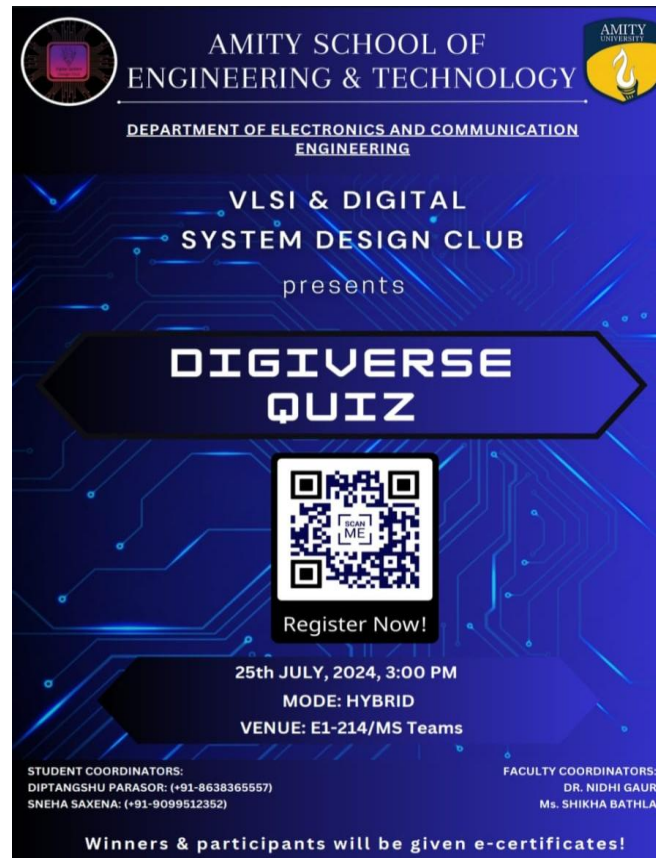


3. **VLSI UNVEILED:** "VLSI Unveiled" was an insightful event held on March 7th, 2024, from 2 PM to 3 PM in E1-309 at Amity University Uttar Pradesh. Attendees had the opportunity to engage in discussions and explore the latest developments in Very Large-Scale Integration (VLSI) technology. Through the session, participants gained valuable insights into the advancements, trends, and practical applications in the field of VLSI. The event fostered a collaborative environment, allowing attendees to exchange ideas and perspectives on VLSI design and implementation, contributing to their knowledge and understanding of this critical domain



## Events organized for the academic year 2024-2025:

1. **DIGIVERSE QUIZ:** The Digiverse Quiz, a notable event centered on Very Large Scale Integration (VLSI) technology, organized by took place on July 25, 2024, at Amity University Uttar Pradesh (AUUP) organized by the VLSI and Digital System Design Club, Electronics and Communication Department, AUUP Noida. The quiz aimed to enhance participants' knowledge and practical skills in VLSI, promoting a deeper understanding and appreciation of advanced electronics and semiconductor design.

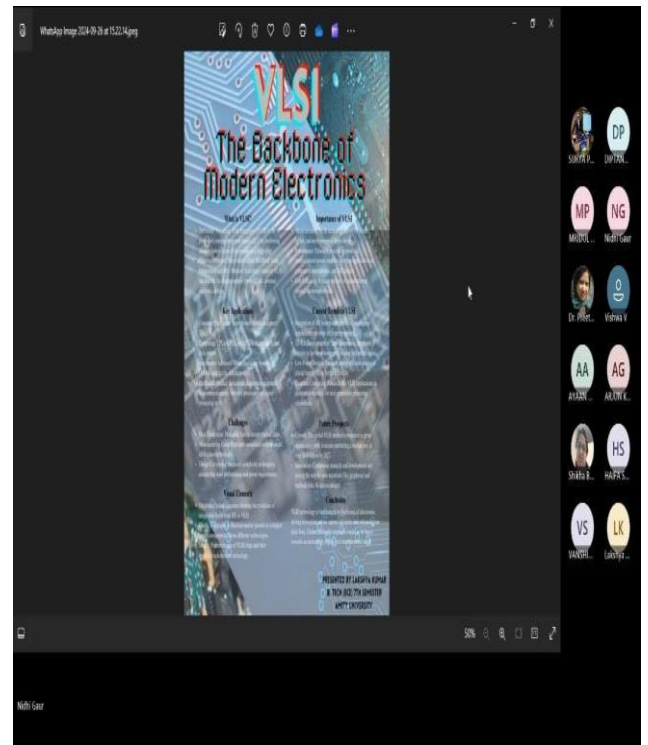


2. **SYNTAX SMACKDOWN** : Syntax Smackdown event, hosted by the VLSI and Digital Design Club on 8th August 2024, was a technical competition that successfully tested students' skills in digital circuit design and coding held at 3 PM in E1 214, Amity University Noida. The competition featured 18 participants, who were tasked with writing code for a given combinational circuit.



3. **IDEATHON** : The IDEATHON event, organized by the VLSI and Digital System Design Club, took place on 26th September 2024 at 2:30 PM in E1 214, Amity University Noida. The event was coordinated by Dr. Nidhi Gaur and Ms. Shikha Bathla, with Dr. Preeti Singh Bahadur, Associate Professor in the Applied Science Department at Amity University Greater Noida, serving as the jury member. This unique event aimed to inspire students to present their ideas for the future of intelligent hardware in the field of VLSI. Participants from various campuses of AUUP including Noida, Greater Noida and Lucknow took part in the competition, showcasing innovative solutions through poster presentations. Their ideas ranged from energy-efficient VLSI designs to advanced machine learning integration, offering forward-thinking solutions for modern hardware challenges. Event gained participation from various fraternity of domain including ECE, CSE, AIIT, EEE, and Robotics (ASET-Noida, AIIT- Noida, ASET- Greater Noida etc.)





4. **De-FAULT** : The VLSI and Digital System Design Club of Amity University, Uttar Pradesh, hosted De-FAULT: Physical VLSI Debug and Design Contest on October 10, 2024. The event, attended by 25 participants, featured a dynamic quiz focused on debugging VHDL and Verilog codes, fostering critical thinking and technical expertise in physical VLSI design. The event was graced by Chief Guest Ashutosh Gupta, a VLSI expert with over 13 years of experience in academia and industry, currently working as a Staff Engineer at Synopsys India Pvt Ltd. His insights emphasized the importance of mastering debugging skills in modern VLSI design.





5. **The Circuit Cipher:** The VLSI and Digital System Design Club of Amity University, Uttar Pradesh, hosted *The Circuit Cipher: Circuit decoding Contest* on 09<sup>th</sup> JANUARY 2025. The event, registered by more than 20 participants, featured a dynamic quiz focused on decoding digital electronic circuits and logic elements, fostering critical thinking and technical expertise in digital circuit design. The event was graced by Chief Guest and Jury for the competition, Dr. Pradeep Kumar, a VLSI and digital design expert with over 20 years of experience in academia, currently working as a Professor and Deputy Head, Department of ECE, ASET. His insights emphasized the importance of mastering debugging skills in modern digital circuit design.



6. **Design Frontier: Innovations in CAD Tools for Digital System Design :** The VLSI & Digital System Design Club organized an engaging Poster Making Competition on "Innovations in CAD Tools for Digital System Design" on 30th Jan, 2025 at MS Teams/ E1-214. The event aimed to foster creativity and technical awareness among students by encouraging them to visualize and present advancements in Computer-Aided Design (CAD) tools for digital system design.

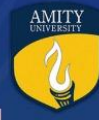


7. **Guest Lecture on “The Road to become a chip architect”:** The VLSI & Digital System Design Club organized a Guest Lecture on “The Road to become a chip architect” on 27<sup>th</sup> Feb, 2025. The lecture was delivered by two professionals from the Industry: Mr. Madhur Maheshwari and Mr. Kapil Saxena. Student's and other participants have gained inside out knowledge of how to become Chip Architect and frame future in VLSI Chip Design field. Students had an opportunity to interact with industry professionals and get solution to their queries. Further the quiz competition at the end has helped them measure the depth of knowledge seed sown in.



AMITY SCHOOL OF  
ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
ENGINEERING



VLSI & DIGITAL SYSTEM DESIGN CLUB

presents

## INDUSTRY GUEST LECTURE

"The Road to  
Become a Chip  
Architect"



**Mr. Madhur Maheshwari**

Associate Vice President  
Self Business



**Mr. Kapil Saxena**

Sr. Engineering Director & BU Head  
ASIC Physical Design, cinfochips



27th  
Feb  
2025



2:30  
pm



Online MS Teams



Scan Now

To Register!

STUDENT COORDINATORS:

SNEHA SAXENA : (+91 9099512352)

FACULTY COORDINATORS:

DR. NIDHI GAUR  
Ms. SHIKHA BATHLA